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Patent

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  Takashi Kumamoto, et al.  )	Examiner: A. Chambliss	
Application No.: 09/893,588 )	Art Unit: 2827	
Filing Date: June 29, 2001 )	I hereby certify that this correspondence is being with the United States Postal Service as first class sufficient postage in an envelope addressed to the Co	s mail with
For: Multi-Purpose Planarizing/Back-grind/Pre- Underfill Arrangements for Bumped Wafers and	for Patents, PO Box 1450, Alexandria, Virginia a on 33104	<b>22</b> 313-1450
)	Debral. Milbourne	
Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450	Name of Rerson Mailing Correspondence S Signature Da	31/04

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The assignee, Intel Corporation, of the above-identified Patent Application, hereby revokes all previous powers of attorney given in this Patent Application, and appoints the firm identified below and individual.

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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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Date: 1000 30, 2004

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